GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - III EXAMINATION HELD IN NOVEMBER 2017   Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3301   P R No : 201203659   Sex : F   Name : NAIK ASHIKA RAMAKANT

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
System on Chip (SOC)					
	Theory	4	AB	P	
	IA	2	AO	P	
TCP/IP and Implementation of a Network IP on FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Project					
	IA	4	AA	P	
	Oral	8	AA	P	
	Total :	24			8.92 P
					PASSES

Seat No : 3302   P R No : 201107721   Sex : F   Name : PHADTE PRIYANKA DHARMA

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
System on Chip (SOC)					
	Theory	4	AB	P	
	IA	2	BC	P	
TCP/IP and Implementation of a Network IP on FPGA					
	Theory	4	CC	P	
	IA	2	BC	P	
Project					
	IA	4	BB	P	
	Oral	8	CC	P	
	Total :	24			6.00 P
					PASSES

Seat No : 3303   P R No : 201611666   Sex : F   Name : RASHINKAR PRAGATI GANAPATHI

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
System on Chip (SOC)					
	Theory	4	BB	P	
	IA	2	AB	P	
TCP/IP and Implementation of a Network IP on FPGA					
	Theory	4	BB	P	
	IA	2	BB	P	
Project					
	IA	4	AB	P	
	Oral	8	BC	P	
	Total :	24			6.92 P
					PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college

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Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - III EXAMINATION
HELD IN NOVEMBER 2017   Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3304   P R No : 201107728   Sex : F   Name : RODRIGUES FROILA VALENCY

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
Image Processing					
	Theory	4	AB	P	
	IA	2	AB	P	
TCP/IP and Implementation of a Network IP on FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Project					
	IA	4	AA	P	
	Oral	8	AA	P	
	Total :	24			8.75 P
					PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By :

Checked By :

Date : 28/2/2018

Vivek Salaskar
Assistant Registrar-E(Proff.)

Controller Of Examinations

Prof. Y. V. Reddy
Registrar

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average;
CGPA: Cummulative Grade Point Average

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